# **BINGYAO LI**

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# EDUCATION

| Aug. 2020 – Now       | <b>University of Pittsburgh</b><br>Ph.D. in Computer Science<br>Advisor: Dr. Xulong Tang                         |
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| Sep. 2017 – Jan. 2020 | <b>Tianjin University</b><br>M.S. in Computer Science and Technology<br>Advisor: Dr. Ce Yu, Graduated with Honor |
| Sep. 2013 – July 2017 | <b>Tianjin University</b><br>B.E. in Computer Science and Technology<br>Graduated with Honor                     |

#### **RESEARCH EXPERIENCE**

| May 2024 - Aug. 2024  | <b>NVIDIA Research</b> , Architecture Research Group<br>Research Intern working on KV-cache management for LLM inference<br>Mentor: Dr. Aamer Jaleel |
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| June 2019 - Aug. 2019 | <b>ICT of Chinese Academy of Science</b><br>Visiting Scholar working on benchmarking RISC-V architecture<br>Mentor: Dr. Yungang Bao                  |

## CONFERENCE PUBLICATIONS

 $^{\ast}$  The authors contribute equally.

| HPCA 2025  | Yueqi Wang, <b>Bingyao Li</b> , Mohamed Tarek Ibn Ziad, Lieven Eeckhout, Jun Yang, Aamer Jaleel, Xulong Tang, "OASIS: Object-Aware Page Management for Multi-GPU Systems", <i>The 31th IEEE International Symposium on High-Performance Computer Architecture</i> , 2025.              |
|------------|--|
| MICRO 2024 | <b>Bingyao Li</b> , Yueqi Wang, Tianyu Wang, Lieven Eeckhout, Jun Yang, Aamer Jaleel, Xulong Tang, "STAR: Sub-Entry Sharing-Aware TLB for Multi-Instance GPU", In Proceedings of the 57th IEEE/ACM International Symposium on Microarchitecture, 2024.                                 |
| HPCA 2024  | Yueqi Wang <sup>*</sup> , <b>Bingyao Li</b> <sup>*</sup> , Aamer Jaleel, Jun Yang, Xulong Tang, "GRIT: Enhancing Multi-<br>GPU Performance with Fine-Grained Dynamic Page Placement", <i>The 30th IEEE International</i><br>Symposium on High-Performance Computer Architecture, 2024. |
| MICRO 2023 | Bingyao Li, Yanan Guo, Yueqi Wang, Aamer Jaleel, Jun Yang, Xulong Tang, "IDYLL: Enhancing Page Translation in Multi-GPUs via Light Weight PTE Invalidations", In Proceedings of the 56th IEEE/ACM International Symposium on Microarchitecture, 2023.                                  |
| HPCA 2023  | Bingyao Li, Jieming Yin, Anup Holey, Youtao Zhang, Jun Yang, Xulong Tang, "Trans-FW: Short Circuiting Page Table Walk in Multi-GPU Systems via Remote Forwarding", <i>The 29th IEEE International Symposium on High-Performance Computer Architecture</i> , 2023.                      |
| DAC 2023   | <b>Bingyao Li</b> , Yueqi Wang, Xulong Tang, "Orchestrated Scheduling and Partitioning for Improved Address Translation in GPUs", <i>The 60th Design Automation Conference</i> , 2023.   |

| WWW 2022    | <b>Bingyao Li</b> <sup>*</sup> , Qi Xue <sup>*</sup> , Geng Yuan <sup>*</sup> , Sheng Li, Xiaolong Ma, Yanzhi Wang and Xulong Tang,<br>"Optimizing Data Layout for Training Deep Neural Networks", <i>The ACM Web Conference Workshop</i> , 2022.  |
|-------------|--|
| MICRO 2021  | Bingyao Li, Jieming Yin, Youtao Zhang, Xulong Tang, "Improving Address Translation<br>in Multi-GPUs via Sharing and Spilling aware TLB Design", In Proceedings of the 54th<br>IEEE/ACM International Symposium on Microarchitecture, 2021.   |
| ICA3PP 2018 | <b>Bingyao Li</b> , Ce Yu, Xiaoteng Hu, Jian Xiao, Shanjiang Tang, Lianmeng Li, Bin Ma, "An Efficient Retrieval Method for Astronomical Catalog Time Series Data", <i>The 18th International Conference on Algorithms and Architectures for Parallel Processing</i> , 2018               |
| HPCC 2018   | Xiaoteng Hu, Ce Yu, <b>Bingyao Li</b> , Shanjiang Tang, Jian Xiao, Yanyan Huang, "GAIDR: An Efficient Time Series Subsets Retrieval Method for Geo-Distributed Astronomical Data", <i>The 20th IEEE International Conference on High Performance Computing and Communications</i> , 2018 |
| JOURNAL     |  |
| PASP 2019   | <b>Bingyao Li</b> , Ce Yu, Chen Li, Xiaoteng Hu, Jian Xiao, Shanjiang Tang, Chenzhou Cui, and Dongwei Fan, "mcatCS: A Highly Efficient Cross-Matching Scheme for Multi-Band Astronomical Catalogs", <i>Publication of the Astronomical Society of the Pacific</i> , 2019, 131(999).      |
| EA 2019     | Ce Yu, <b>Bingyao Li</b> , Jian Xiao, Chao Sun, Shanjiang Tang, Chongke Bi, Chenzhou Cui, and Dongwei Fan, "Astronomical Data Fusion: Recent Progress and Future Prospects - A Survey", <i>Springer Experimental Astronomy</i> , 2019(6).  |
| PATENTS     |  |

| 2024 | <b>Bingyao Li</b> , Aamer Jaleel, Po-An Tsai, Anish Saxena, "Dynamic Key-Value Cache Scheduling for LLM Serving", NVIDIA      |
|------|---|
| 2024 | Anish Saxena, Po-An Tsai, <b>Bingyao Li</b> , Aamer Jaleel, "Restricted Expert Mixture Framework for MoE LLM Serving", NVIDIA |

# HONORS & AWARDS

| 2024, 2023, 2022 | Student Travel Grant, MICRO                                    |
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| 2025, 2024, 2023 | Student Travel Grant, HPCA                                     |
| 2023, 2022       | CS50 Outstanding Research Fellowship, University of Pittsburgh |
| 2023             | PhD Forum at MICRO   |
| 2022             | Student Travel Grant, ISCA                                     |
| 2020             | SCI Research Fellowship, University of Pittsburgh              |
| 2019             | National Scholarship, Ministry of Education of China           |
| 2019, 2017       | Graduate Scholarship – First Prize, Tianjin University         |

# **RESEARCH TALKS**

| 2025 | <b>Unleashing Multi-GPU Computing to the Next-Level</b><br>at Binghamton University, UT Dallas, UC Riverside, ASU, William&Mary,<br>UMass Amherst, Purdue University, and Duke University |
|------|---|
| 2024 | <b>STAR: Sub-Entry Sharing-Aware TLB for Multi-Instance GPU</b> at MICRO 2024, Austin, TX   |

| 2024, 2023 | Towards Efficient and Salable Computing for Multi-GPUs<br>at NVIDIA, USA<br>at Tianjin University, China                |
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| 2024       | <b>GRIT: Enhancing Multi-GPU Performance with Fine-Grained Dynamic Page Placement</b><br>at HPCA 2024, Edinburgh, UK    |
| 2023       | IDYLL: Enhancing Page Translation in Multi-GPUs via Light Weight PTE Invalidations<br>at MICRO 2023, Toronto, ON        |
| 2023       | <b>Orchestrated Scheduling and Partitioning for Improved Address Translation in GPUs</b> at DAC 2023, San Francisco, CA |
| 2023       | Trans-FW: Short Circuiting Page Table Walk in Multi-GPU Systems via Remote Forwarding at HPCA 2023, Montreal, QC        |
| 2022       | <b>Optimizing Data Layout for Training Deep Neural Networks</b><br>at WWW 2022, Virtual                                 |
| 2021       | Improving Address Translation in Multi-GPUs via Sharing and Spilling aware TLB Design<br>at MICRO 2021, Virtual         |

### ACADEMIC SERVICES

| Reviewer                         | The International Conference on Learning Representations (ICLR 2025)   |
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| Journal Reviewer                 | IEEE Transactions on Computers (TC)<br>ACM Transactions on Architecture and Code Optimization (TACO)<br>Journal of Systems Architecture (JSA)<br>Journal of Combinatorial Optimization (JOCO)      |
| Artifact Evaluation<br>Committee | IEEE/ACM International Symposium on Microarchitecture (MICRO 2024, 2022)<br>ACM International Conference on Architectural Support for Programming Languages and<br>Operating Systems (ASPLOS 2023) |

## TEACHING EXPERIENCE

Fall 2023, 2022Guest Lecturer for Computer Architecture, CS 2410, PittFall 2021Teaching Assistant of Introduction to Operating Systems, CS 1550, Pitt